

WEST Search History

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DATE: Tuesday, July 31, 2007

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L21	((delay\$3 same invert\$3) near6 (((pulse near2 width\$) or (duty near3 cycle\$))near2 modulat\$3) or pwm) and (full near2 bridge\$)	20
<input type="checkbox"/>	L20	((delay\$3 same invert\$3) near4 (((pulse near2 width\$) or (duty near3 cycle\$)) near2 modulat\$3) or pwm)) and (driver\$3 near4 full near2 bridge\$)	0
<input type="checkbox"/>	L19	((dely\$3 same invert\$3) near4 (((pulse near2 width\$) or (duty near3 cycle\$)) near2 modulat\$3) or pwm)) and (driver\$3 near4 full near2 bridge\$)	0
<input type="checkbox"/>	L18	((dely\$3 same invert\$3) near4 (((pulse near2 width\$) or (duty near3 cycle\$)) near2 modulat\$3) or pwm)) same (driver\$3 near4 full near2 bridge\$)	0
<input type="checkbox"/>	L17	(L16 and l15 and l2) not l14	108
<input type="checkbox"/>	L16	375/\$3.ccls. or 327/\$4.ccls. or 329/\$3.ccls. or 332/\$4.ccls. or 370/\$4.ccls. or 381/\$5.ccls. or 307/\$4.ccls.	381271
<input type="checkbox"/>	L15	(((pulse near2 width\$) or (duty near3 cycle\$)) near2 modulat\$3) or pwm) same (full near2 bridge\$)	1305
<input type="checkbox"/>	L14	L13 and L2	46
<input type="checkbox"/>	L13	(((pulse near2 width\$) or (duty near3 cycle\$)) near2 modulat\$3) or pwm) same (driver\$3 near4 full near2 bridge\$)	65
<input type="checkbox"/>	L12	L11 and L7 and L2	24
<input type="checkbox"/>	L11	375/\$3.ccls. or 327/\$4.ccls. or 329/\$3.ccls. or 332/\$4.ccls. or 370/\$4.ccls.	286928
<input type="checkbox"/>	L10	L7 and L4 and L2	2
<input type="checkbox"/>	L9	L2 and L4 and L3	0
<input type="checkbox"/>	L8	((multi\$5 or plurla\$4) near3 ((pulse near2 width\$ near2 modulat\$3) or pwm)) and (driver\$) and (full near2 bridge\$) and (inverter\$3) and delay	29
<input type="checkbox"/>	L7	((pulse near2 width\$ near2 modulat\$3) or pwm) and (driver\$) and (full near2 bridge\$) and (inverter\$3) and delay	448
<input type="checkbox"/>	L6	((pulse near2 width\$ near2 modulat\$3) or pwm) and (driver\$) and (full near2 bridge\$)	1664
<input type="checkbox"/>	L5	L4 and L3	0
<input type="checkbox"/>	L4	(375/238).ccls. or (327/31).ccls. or (327/38).ccls. or (329/312).ccls. or (329/311).ccls. or (332/109).ccls. or (332/106).ccls. (370/205).ccls.	2776
<input type="checkbox"/>	L3	L2 and L1	146
<input type="checkbox"/>	L2	@ad<20040421	25871652
<input type="checkbox"/>	L1	((pulse near2 width\$ near2 modulat\$3) or pwm) same (driver\$) same (full near2 bridge\$)	201